

InAs_{0.7}Sb_{0.3} Bulk Photodiodes Operating at Thermoelectric-Cooler Temperatures

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Current–voltage and photoelectrical characteristics of InAs_{0.7}Sb_{0.3} photodiodes grown onto InAs substrates are investigated in the interval of 212–330 K, i.e., the “thermoelectrical temperature range.” The impacts of mesa diameter, buffer layer thickness, and cooling on the zero-bias resistance and spectral responsivity are described and analyzed. At low temperatures, the dynamic zero-bias resistance dominates the serial one, resulting in the specific detectivity at 6.5 μm and at $T = 233$ K being as high as $3.2 \cdot 10^8 \text{ cm} \cdot \text{Hz}^{1/2} \cdot \text{W}^{-1}$ for a flat-plate photodiode.

This makes InAs-based PDs “blind” to high-power medical lasers operating in the 2 μm range and thus allows simultaneous efficient delivery of high power through a fiber and precise detection of thermal radiation intensity and temperature measurements of the fiber tip performed in the 3–5 μm spectral range.

In this paper, we study InAs_{0.7}Sb_{0.3} PDs grown onto InAs substrates, with an emphasis on an impact of mesa diameter, buffer layer thickness, and cooling on the zero-bias resistance and spectral responsivity values mainly in the temperature range of 212–330 K, usually referred to as the “thermoelectrical temperature range.”

1. Introduction

CdHgTe and InAsSb alloys have been traditionally used for many years as “bulk” absorbing layers in barrier and conventional mid-IR photodiodes (PDs) that have found applications in gas sensing,^[1] IR thermometry,^[2] and thermal imaging. Both alloys have a specific band structure that, despite the misfit dislocations formed during the growth on lattice mismatched substrates, allows for p-n structures with a low dark current and high quantum efficiency (see, e.g., Refs. [3–8]). For InAsSb-based structures, the above property is believed to be enabled by their unique property of producing electronic states above the conduction band, thereby suppressing the Shockley–Read–Hall recombination processes.^[9,10] There is thus a good chance for manufacturing efficient PDs for radiation detection at wavelengths exceeding 6 μm by using lattice mismatched substrates, e.g., InAs substrates for the InAs_{1-x}Sb_x/InAs ($x > 0.2$, $\Delta a/a > 0.015$) heterostructure PDs. In certain cases, the InAs_{1-x}Sb_x/InAs structures are beneficial with respect to InAs_{1-x}Sb_x/GaSb structures because the former offer narrow bandwidth when illuminated from a side of the InAs substrate that cuts off radiation with wavelengths shorter than ≈ 3 μm.

2. Experimental Methods and Sample Description

Shown in **Figure 1** are the chemical composition, interpolated energy gap $E_g^{300\text{K}}(X,Y)$, and lattice constant mismatch ($8a/a$) of the InAs_{1-x-y}Sb_xP_y alloy versus distance in a typical structure grown onto InAs substrate using a liquid-phase epitaxy method. The heterostructures with a 4–9 μm thick n-InAsSb buffer layer, a 3 μm thick n-InAsSb active layer, and a 3 μm thick P-InAsSbP(Zn) contact layer were processed using standard optical photolithography into rectangular flip-chip PDs. The PD chips of design described elsewhere^[11] include an ≈ 100 μm thick n-InAs substrate, a circular broad metal anode on top of 35–250 μm wide circular active areas (mesas) with an unpassivated sidewall and a “horse shoe” metal cathode on the n-InAs substrate. Usually, there is sufficient spatial redistribution of radiation in flip-chip PDs^[11]; thus, for the sake of the adequate optical area/responsivity evaluation, some PDs chips were additionally equipped with an opaque diaphragm with known open area or a hyperhemispherical Si immersion lens ($\varnothing = 3.5$ mm) glued onto the InAs substrate via a layer of chalcogenide glass with $n = 2.4$.

Figure 2(a) demonstrates room temperature (RT) heterostructure (wafer) transparency and electroluminescence (EL) and negative luminescence (NL) spectra both peaked at the photon energy of 180 meV, which is quite close to the InAs_{0.7}Sb_{0.3} interpolated energy gap. L – I characteristics for both NL and EL operation modes (see **Figure 2(b)**) were almost linear, with a peak power of 3 μW at a forward current of $I = 1$ A (0.003 mW A^{-1}) at RT for the diode with the immersion lens. The latter value is

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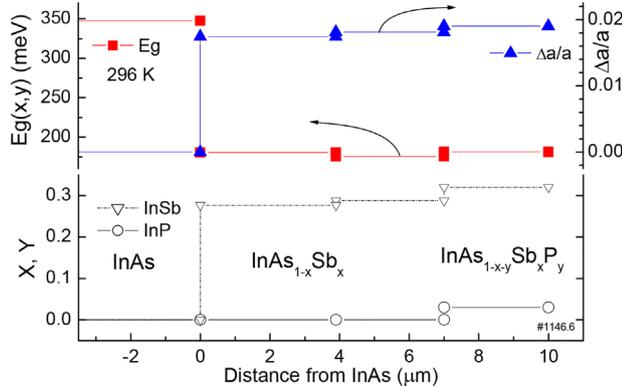


Figure 1. Mole fraction of InSb (X , ∇), InP (Y , \circ) and simulated energy gap $E_g(X, Y)$ (\blacksquare) and lattice mismatch with the InAs substrate ($\Delta a/a$, \blacktriangle) at room temperature versus distance in InAsSb_{0.3} PD.

- 1 close to the power of the uncoated InAsSb_{0.44} “barrier LEDs”
- 2 with similar spectral characteristics when cooled down to 77 K in
- 3 Ref. [12].

4 3. Results and Discussion

5 **Figure 3** shows the “raw data” zero-bias resistance-area product ($R_0A = A \times dU/dI$) at RT as a function of mesa diameter D_m . As
6 seen from the data in Figure 3, the R_0A product progressively
7 decreases and conductivity increases as the PD lateral
8 dimensions approach zero, indicating the presence of both
9 bulk and surface leakage currents. The leakage current density
10 was evaluated in a manner similar to that presented in Ref. [13],
11 that is, from the dependence of a total dark current on mesa
12 diameter. The corresponding data together with best fit curves
13 presenting the simulated bulk I_{p-n} , the surface leakage $I_{surf.}$, and
14 the total ($I_{p-n} + I_{surf.}$) currents at $U = -0.1$ V and $U = -0.2$ V are
15 shown in **Figure 4**. As seen from Figure 4, the surface leakage
16 current ($J_{surf.} = 0.1$ A cm⁻¹ at $U = -0.1$ V) dominates the bulk
17 one starting at mesa diameter less than ≈ 17 μ m.

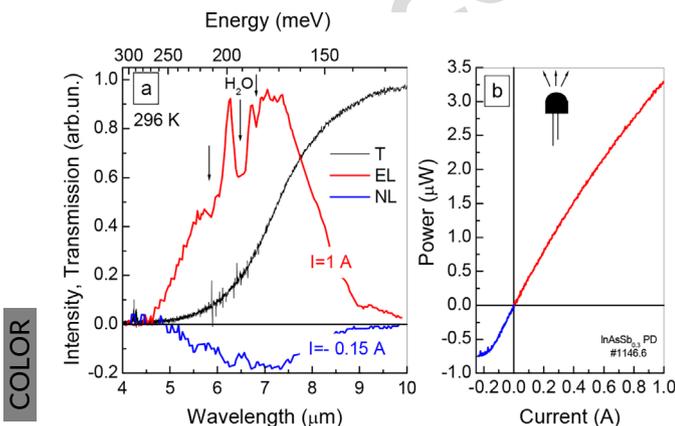


Figure 2. Room temperature EL and NL spectra together with the p-n structure transmission (a) and the I - L characteristic of the immersion lens PDs (b).

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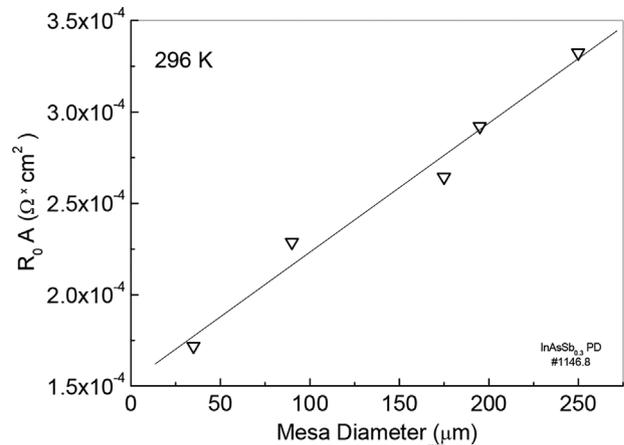


Figure 3. “Raw data” zero-bias resistance-area product (R_0A) versus mesa diameter D_m .

In general, an RT serial resistance R_s in narrow gap structures
1 of a large lateral size is usually much greater than the dynamic
2 p-n junction resistance, resulting in a nearly linear I - V
3 characteristic for most forward-bias voltages $U_{forw.}$ (see, e.g.,
4 Ref. [14]). In such cases the I - V characteristic relative solely to
5 the p-n junction properties could be obtained by extracting the
6 voltage drop across the serial resistance in the following manner:
7 $U_{p-n} = U_{forw.} - R_s I$, where U_{p-n} is understood as a voltage across
8 the p-n junction, and $R_s = dU_{forw.}/dI$ at $U_{forw.} > 0.1$ V. Shown in
9 **Figure 5** are the I - V_{p-n} characteristics of the p-n junction in a
10 195 μ m wide PD with $R_s \approx 1$ Ω measured at several tempera-
11 tures achievable by a 2-stage thermoelectric cooling. As shown
12 in Figure 5, the I - V_{p-n} characteristics at 212, 233, 256, 273, and
13 295 K adequately meet the modified Shockley formula
14 $I = I_0 \cdot [\exp(eU_{p-n}/\beta kT) - 1]$, where e is the elementary charge,
15 β is the ideality factor, k is the Boltzmann constant, and T is the
16 temperature. The ideality factor β ranged from unity at $T = 295$ K
17

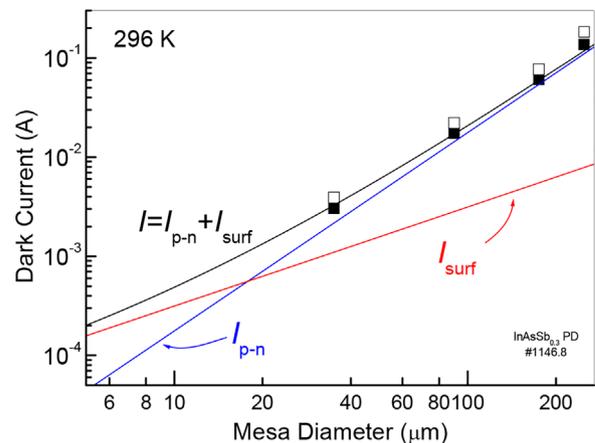


Figure 4. Total diode dark current at $U = -0.1$ V (\blacksquare) and -0.2 V (\square) (b) in InAsSb_{0.3} PDs at room temperature. The lines present the simulated bulk ($I_{p-n} = J_{p-n} \times \pi \times (D_m/2)^2$, $J_{p-n} = 56$ A cm⁻²) and surface leakage ($I_{surf.} = J_{surf.} \times \pi \times D_m$, $J_{surf.} = 0.1$ A cm⁻¹) currents as well the sum of the above two currents ($I_{p-n} + I_{surf.}$) at $U = -0.1$ V.

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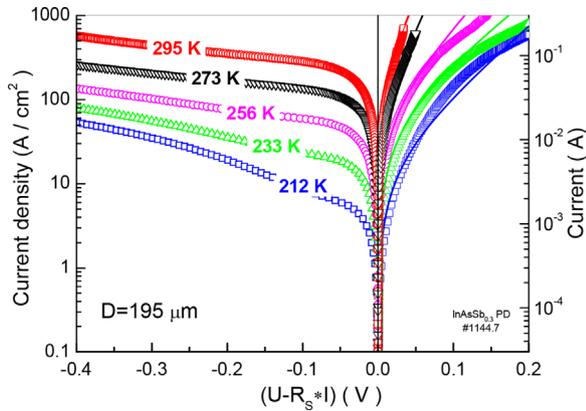


Figure 5. Dark current in InAsSb_{0.3} PDs with an 8.8 μm thick buffer layer versus bias applied to the p–n junction U_{p-n} at 212, 233, 256, 273, and 295 K. Solid lines represent the modified Shockley function.

1 addition, at RT, the $R_{p-n}A$ value is not far from the expectations 1
2 based on simulations made by Wróbel in Ref. [15].

3 When comparing InAs_{0.7}Sb_{0.3} PDs with other detector types, 3
4 note that in some cases, the R_oA or $R_{p-n}A$ value does not provide 4
5 adequate performance characterization. For example, barrier 5
6 detector current responsivity at small bias usually approaches 6
7 zero; as a result, normally, the figure of merit is the dark current 7
8 at a certain bias and not the value of R_oA . The 233 K dark current 8
9 value J_{dark} in our PDs was several times smaller than that in 9
10 InAsSb_{0.4} barrier photodiodes at $U = -0.45$ V with similar 10
11 spectral response^[8,9] and was ≈ 10 times higher than the values 11
12 derived from the “Rule 07” directives.^[16] Shown in Figure 6 is the 12
13 temperature dependence of the J_{dark}/T^3 ratio at biases of -0.1 13
14 and -0.4 V, where the term T^3 accounts for the temperature 14
15 dependence of the density of states in the expression for the 15
16 diffusion current density.^[8] The activation energy of the J_{dark}/T^3 16
17 exponential dependence at $U = -0.1$ V and $T > 230$ K was close 17
18 to that of the $R_{p-n}A$, that is, it was approximately two times higher 18
19 than that expected for a PD with dominant generation- 19
20 recombination current.

21 It is generally anticipated that the dark current in lattice- 21
22 mismatched PD structures declines with increasing buffer layer 22
23 thickness (see, e.g., Refs. [17,18]). In our case, however, the above 23
24 “rule of thumb” was not very well pronounced, as only minor PD 24
25 zero-bias resistance and sensitivity enhancement in PDs with 25
26 thick buffer could be traced from the data in Figure 7(b) and (c). 26
27 In contrast, a remarkable change in spectral response was 27
28 observed: the relative responsivity at short waves progressively 28
29 increased as the buffer layer thickness (t) decreased via the 29
30 collection efficiency enhancement in “thin buffer” structures, 30
31 e.g., from 0.3 at $t = 9$ μm up to 0.8 at $t = 4$ μm ($\lambda = 5$ μm) (see 31
32 Figure 7(a)). The responsivity dependence on t suggests that the 32
33 hole diffusion length L_p and a distance between the InAs/ 33
34 InAsSb and the p–n junctions are of the same order. Draft 34
35 evaluation provides values of $L_p^{300K} \approx 6$ μm, which are close to 35
36 the estimations of the same parameter in n-InAsSb-based PDs 36
37 given in Refs. [8,19,20] and approximately two times larger than 37
38 that in Ref. [6].

39 The shortwave responsivity shoulder is obviously related to 39
40 the n-InAs substrate transparency spectrum; the longwave 40

1 to 3 at $T = 212$ K, indicating the dominance of the diffusion 1
2 current near RT and probably the dominance tunneling at 212 K, 2
3 respectively.

4 The prevalence of the diffusion current over other current 4
5 types near RT is also supported by an analysis of the temperature 5
6 dependence of the p–n junction zero-bias resistance-area 6
7 product defined as $R_{p-n}A = \beta kTA/eI_o$, where e is the electron 7
8 charge, and I_o is the “saturation current” parameter derived from 8
9 the best fit of the above Shockley formula and experimental data 9
10 in Figure 5. Indeed, the activation energy of an exponential 10
11 growth of the $R_{p-n}A$ value near RT ($E_a = 234$ meV, see the 11
12 Arrhenius plot in Figure 6) was close enough to the expected 12
13 energy gap value $E_g(x,y) = 175$ meV (see data presented in 13
14 Figure 1). The $R_{p-n}A$ values at $T = 250$ – 300 K appeared higher 14
15 than R_oA published by Razeghi^[3] for the InAsSb/InSb 15
16 heterostructure PDs with nearly similar spectral response; in 16

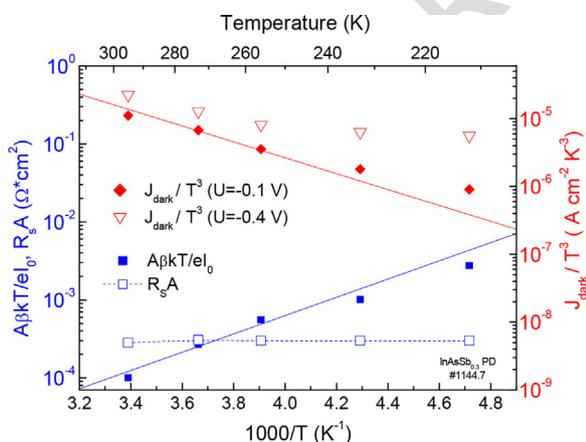


Figure 6. Zero-bias resistance–area product of the p–n junction ($R_{p-n}A$, ■) and serial resistance–area product (R_sA , □) versus reciprocal temperature for a 195 μm wide mesa PD with an 8.8 μm thick buffer layer (all at the left scale) and temperature dependence of the term J_{dark}/T^3 at a reverse bias of -0.1 V (◆) and -0.4 V (▽) (both using the right scale). Solid lines denote the $\exp(E_a/kT)$ and $\exp(-E_a/kT)$ functions.

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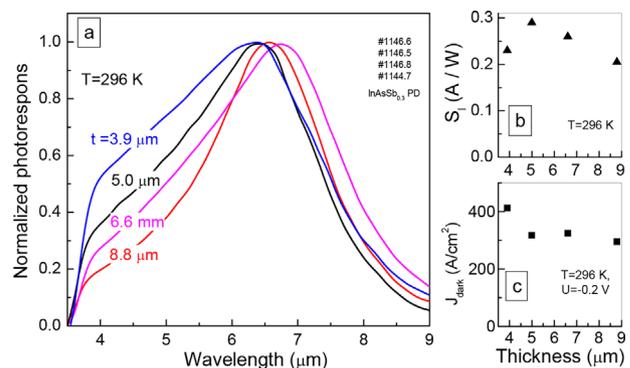


Figure 7. Room temperature normalized photoresponse spectra (a), peak sensitivity (b), and dark current at -0.2 V (c) versus buffer layer thickness (t) in InAsSb_{0.3} PDs.

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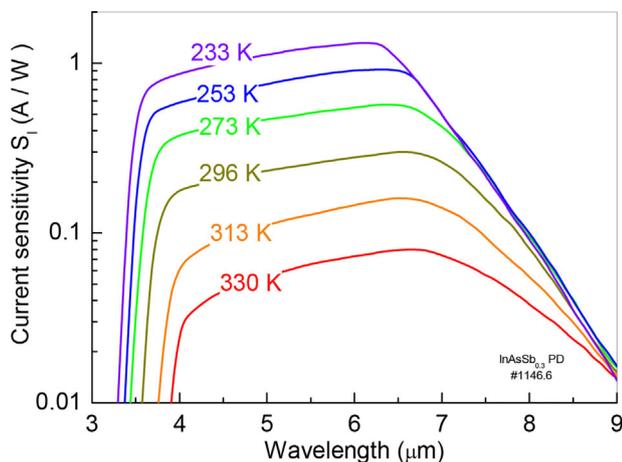


Figure 8. Photoresponse spectra of the PD with 3.9 μm thick buffer layer in the 233–330 K interval.

1 shoulder was temperature-sensitive with a 0.28 meV K^{-1}
 2 temperature shift rate close to InAs energy-gap variation (see
 3 **Figure 8**). The responsivity near the peak wavelength exhibited
 4 nice growth upon cooling of the PD, as shown in Figure 8 and 9;
 5 this result most likely highlights the prevalence of the p–n
 6 junction resistance over R_s at low temperatures. At 233 K, the
 7 $195 \mu\text{m}$ wide mesa PD zero-bias resistance-area product R_0A and
 8 responsivity S_1 at a wavelength of $6.5 \mu\text{m}$ were $0.0015 \Omega \text{ cm}^2$
 9 and 1 A W^{-1} , respectively, which are higher than those in
 10 “commercial” CdHgTe detectors mounted onto a 2-stage
 11 thermoelectric cooler (see, e.g., Ref. [21]). Coherently, the
 12 simulated Johnson noise limited specific detectivity is also
 13 higher than that in Ref. [21] (see **Figure 9**) and at $\lambda = 6.5 \mu\text{m}$
 14 amounts to $3.2 \cdot 10^8 \text{ cm} \cdot \text{Hz}^{1/2} \cdot \text{W}^{-1}$ for a bare chip PD.
 15 Incorporation of a hyperhemispherical immersion Si lens
 16 ($\varnothing = 3.5 \text{ mm}$) increases the $D^*_{6.5 \mu\text{m}, 233 \text{ K}}$ up to $4.3 \cdot 10^9 \text{ cm} \cdot$

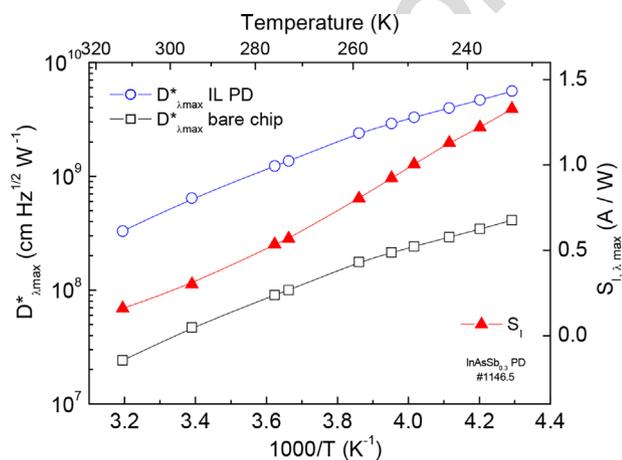


Figure 9. Specific detectivity D^* for bare chip (\square) and immersion lens (\circ) PDs (left scale) and responsivity (right scale, \blacktriangle) at maximum versus temperature in a $195 \mu\text{m}$ wide bare chip $\text{InAsSb}_{0.3}$ PD with $5 \mu\text{m}$ thick buffer layer.

$\text{Hz}^{1/2} \cdot \text{W}^{-1}$, making $\text{InAsSb}_{0.3}$ PDs superior to the commercial
 PDs, at least at wavelengths of $\approx 6.5 \mu\text{m}$.

4. Conclusions

$\text{InAsSbP}/\text{InAsSb}_{0.3}$ p–n heterostructures grown onto InAs
 substrates demonstrated diffusion-limited current at near room
 temperature and tunnel current at 212–250 K with predomi-
 nantly series resistance in the temperature range of 270–300 K.
 The zero-bias p–n junction resistance-area product was close to
 the theoretical estimations, and the specific detectivity at 233 K
 achievable by a 2-stage thermoelectric cooler amounted to
 remarkable values of $D^*_{6.5 \mu\text{m}} = 3.2 \cdot 10^8$ and $4.3 \cdot 10^9$ Jones
 for the $195 \mu\text{m}$ wide bare chip and immersion lens PDs ($\varnothing =$
 3.5 mm), respectively.

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Conflict of Interest

The authors declare no conflict of interest.

Keywords

backside illuminated photodiodes, dark current, InAsSb photodiodes, infrared sensors, IR gas sensors, mid-IR detectors, pyrometry

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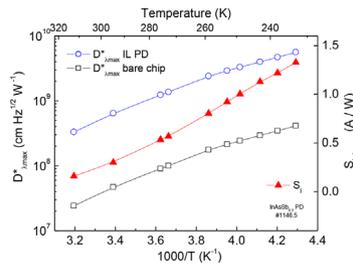
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Low energy gap flip-chip photodiodes with InAsSb_{0.3} absorbing layer have been fabricated. These photodiodes exhibit reasonably high values of specific detectivity and responsivity at 6.5 μm when cooled down to 233 K which is essential for many spectroscopic measurements including pyrometry and gas analysis.

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